



# **AOS Semiconductor Product Reliability Report**

**AO4824/AO4824L, rev C**

**Plastic Encapsulated Device**

**ALPHA & OMEGA Semiconductor, Inc**

**495 Mercury Drive  
Sunnyvale, CA 94085  
U.S.**

**Tel: (408) 830-9742**

**[www.aosmd.com](http://www.aosmd.com)**

**Nov 7, 2005**

This AOS product reliability report summarizes the qualification result for AO4824. Accelerated environmental tests are performed on a specific sample size, and then followed by electrical test at end point. Review of final electrical test result confirms that AO4824 passes AOS quality and reliability requirements. The released product will be categorized by the process family and be monitored on a quarterly basis for continuously improving the product quality.

### Table of Contents:

- I. Product Description
- II. Package and Die information
- III. Environmental Stress Test Summary and Result
- IV. Reliability Evaluation
- V. Quality Assurance Information

### I. Product Description:

The AO4824 uses advanced trench technology to provide excellent  $R_{DS(ON)}$  and low gate charge. The two MOSFETs make a compact and efficient switch and synchronous rectifier combination for use in DCDC converters. Standard Product AO4824 is Pb-free (meets ROHS & Sony 259 specifications). AO4824L is a Green Product ordering option. AO4824 and AO4824L are electrically identical.

Absolute Maximum Ratings $T_A=25^\circ\text{C}$ unless otherwise noted				
Parameter		Symbol	Maximum	Units
Drain-Source Voltage		$V_{DS}$	30	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$T_A=25^\circ\text{C}$	$I_D$	8.5	A
	$T_A=70^\circ\text{C}$		6.8	
Pulsed Drain Current		$I_{DM}$	30	
Power Dissipation	$T_A=25^\circ\text{C}$	$P_D$	2	W
	$T_A=70^\circ\text{C}$		1.28	
Junction and Storage Temperature Range		$T_J, T_{STG}$	-55 to 150	$^\circ\text{C}$

Thermal Characteristics					
Parameter		Symbol	Typ	Max	Units
Maximum Junction-to-Ambient	$t \leq 10\text{s}$	$R_{\theta JA}$	48	62.5	$^\circ\text{C/W}$
	Steady-State		74	110	$^\circ\text{C/W}$
Maximum Junction-to-Lead	Steady-State	$R_{\theta JL}$	35	40	$^\circ\text{C/W}$

## II. Die / Package Information:

	<b>AO4824</b>	<b>AO4824L (Green Compound)</b>
<b>Process</b>	Standard sub-micron low voltage N channel process	Standard sub-micron low voltage N channel process
<b>Package Type</b>	8 leads SOIC	8 leads SOIC
<b>Lead Frame</b>	Copper with Solder Plate	Copper with Solder Plate
<b>Die Attach</b>	Ag epoxy	Ag epoxy
<b>Bond wire</b>	Au 2mils	Au 2 mils
<b>Mold Material</b>	Epoxy resin with silica filler	Epoxy resin with silica filler
<b>Filler % (Spherical/Flake)</b>	90/10	100/0
<b>Flammability Rating</b>	UL-94 V-0	UL-94 V-0
<b>Backside Metallization</b>	Ti / Ni / Ag	Ti / Ni / Ag
<b>Moisture Level</b>	Up to Level 1 *	Up to Level 1 *

Note \* based on info provided by assembler and mold compound supplier

## III. Result of Reliability Stress for AO4824 (Standard) & AO4824L (Green)

Test Item	Test Condition	Time Point	Lot Attribution	Total Sample size	Number of Failures
Solder Reflow Precondition	Standard: 1hr PCT+3 cycle IR reflow@260 °c Green: 168hr 85/85 THB+3 cycle IR reflow@260 °c	0hr	Standard: 48 lots Green: 16 lots	9405 pcs	0
HTGB	Temp = 150 °c , Vgs=100% of Vgsmax	168 / 500 hrs  1000 hrs	2 lots  (note A*)	164 pcs  77+5 pcs / lot	0
HTRB	Temp = 150 °c , Vds=80% of Vdsmax	168 / 500 hrs  1000 hrs	2 lots  (note A*)	164 pcs  77+5 pcs / lot	0
HAST	130 +/- 2 °c , 85%, 33.3 psi, Vgs = 80% of Vgs max	100 hrs	Standard: 30 lots Green: 14 lots  (note B**)	2420 pcs  50+5 pcs / lot	0
Pressure Pot	121 °c , 15+/-1 PSIG, RH=100%	96 hrs	Standard: 48 lots Green: 16 lots  (note B**)	3520 pcs  50+5 pcs / lot	0
Temperature Cycle	-65 to 150 °c , air to air, 0.5hr per cycle	250 / 500 cycles	Standard: 48 lots Green: 15 lots  (note B**)	3465 pcs  50+5 pcs / lot	0

### III. Result of Reliability Stress for AO4824 (Standard) & AO4824L (Green) Continues

<b>DPA</b>	<b>Internal Vision Cross-section X-ray</b>	<b>NA</b>	<b>5 5 5</b>	<b>5 5 5</b>	<b>0</b>
<b>CSAM</b>		<b>NA</b>	<b>5</b>	<b>5</b>	<b>0</b>
<b>Bond Integrity</b>	<b>Room Temp 150°C bake 150°C bake</b>	<b>0hr 250hr 500hr</b>	<b>40 40 40</b>	<b>40 wires 40 wires 40 wires</b>	<b>0</b>
<b>Solderability</b>	<b>230 °c</b>	<b>5 sec</b>	<b>15</b>	<b>15 leads</b>	<b>0</b>
<b>Die shear</b>	<b>150 °c</b>	<b>0hr</b>	<b>10</b>	<b>10</b>	<b>0</b>

**Note A:** The HTGB and HTRB reliability data presents total of available AO4824 and AO4824L burn-in data up to the published date.

**Note B:** The pressure pot, temperature cycle and HAST reliability data for AO4824 and AO4824L comes from the AOS generic package qualification data.

### IV. Reliability Evaluation

**FIT rate (per billion): 32**

**MTTF = 3567 years**

500 hrs of HTGB, 150 deg C accelerated stress testing is equivalent to 15 years of lifetime at 55 deg C operating conditions (by applying the Arrhenius equation with an activation energy of 0.7eV and 60% of upper confidence level on the failure rate calculation). AOS reliability group also routinely monitors the product reliability up to 1000 hr at and performs the necessary failure analysis on the units failed for reliability test(s).

The presentation of FIT rate for the individual product reliability is restricted by the actual burn-in sample size of the selected product (AO4824). Failure Rate Determination is based on JEDEC Standard JESD 85. FIT means one failure per billion hours.

$$\text{Failure Rate} = \text{Chi}^2 \times 10^9 / [2 (N) (H) (Af)]$$

$$= 1.83 \times 10^9 / [2 (164) (168) (258) + 2 (164) (500) (258)] = 32$$

$$\text{MTTF} = 10^9 / \text{FIT} = 3.12 \times 10^7 \text{hrs} = 3567 \text{ years}$$

**Chi<sup>2</sup>** = Chi Squared Distribution, determined by the number of failures and confidence interval

**N** = Total Number of units from HTRB and HTGB tests

**H** = Duration of HTRB/HTGB testing

**Af** = Acceleration Factor from Test to Use Conditions (Ea = 0.7eV and Tuse = 55°C)

Acceleration Factor [**Af**] =  $\text{Exp} [E_a / k ( 1/T_j u - 1/T_j s )]$

**Acceleration Factor ratio list:**

	<b>55 deg C</b>	<b>70 deg C</b>	<b>85 deg C</b>	<b>100 deg C</b>	<b>115 deg C</b>	<b>130 deg C</b>	<b>150 deg C</b>
<b>Af</b>	<b>258</b>	<b>87</b>	<b>32</b>	<b>13</b>	<b>5.64</b>	<b>2.59</b>	<b>1</b>

**Tj s** = Stressed junction temperature in degree (Kelvin), K = C+273.16

**Tj u** = The use junction temperature in degree (Kelvin), K = C+273.16

**k** = Boltzmann's constant, 8.617164 X 10<sup>-5</sup>eV / K



## V. Quality Assurance Information

Acceptable Quality Level for outgoing inspection: **0.1%** for electrical and visual.

Guaranteed Outgoing Defect Rate: **< 25 ppm**

Quality Sample Plan: conform to **Mil-Std-105D**