



AOS Semiconductor Product Reliability Report

AOL1704/AOL1704L, rev B

Plastic Encapsulated Device

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This AOS product reliability report summarizes the qualification result for AOL1704. Accelerated environmental tests are performed on a specific sample size, and then followed by electrical test at end point. Review of final electrical test result confirms that AOL1704 passes AOS quality and reliability requirements. The released product will be categorized by the process family and be monitored on a quarterly basis for continuously improving the product quality.

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I. Product Description:

AOL1704 uses advanced trench technology with a monolithically integrated Schottky diode to provide excellent RDS(ON), and low gate charge. This device is suitable for use as a low side FET in SMPS, load switching and general purpose applications. Standard Product AOL1704 is Pb-free (meets ROHS & Sony 259 specifications).

Absolute Maximum Ratings $T_A=25^{\circ}\text{C}$ unless otherwise noted				
Parameter		Symbol	Maximum	Units
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	± 12	V
Continuous Drain Current	$T_A=25^{\circ}\text{C}$	I_D	50	A
	$T_A=100^{\circ}\text{C}$		43	
Pulsed Drain Current		I_{DM}	120	
Continuous Drain Current	$T_A=25^{\circ}\text{C}$	I_{DSM}	18	
	$T_A=70^{\circ}\text{C}$		14	
Avalanche Current		I_{AR}	25	A
Repetitive avalanche energy $L=0.3\text{mH}$		E_{AR}	94	mJ
Power Dissipation	$T_A=25^{\circ}\text{C}$	P_D	50	W
	$T_A=100^{\circ}\text{C}$		25	
Power Dissipation	$T_A=25^{\circ}\text{C}$	P_{DSM}	4.3	W
	$T_A=70^{\circ}\text{C}$		2.8	
Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 175	$^{\circ}\text{C}$

Thermal Characteristics					
Parameter		Symbol	Typ	Max	Units
Maximum Junction-to-Ambient	$T \leq 10\text{s}$	$R_{\theta JA}$	24	29	$^{\circ}\text{C/W}$
	Steady-State		53	64	$^{\circ}\text{C/W}$
Maximum Junction-to-Lead	Steady-State	$R_{\theta JL}$	2.4	3.0	$^{\circ}\text{C/W}$

II. Die / Package Information:

	AOL1704	AOL1704L (Green Compound)
Process	Standard sub-micron Low voltage N channel process	Standard sub-micron low voltage N channel process
Package Type	Ultra SO-8™	Ultra SO-8™
Lead Frame	Copper	Copper
Die Attach	Soft solder	Soft solder
Bond wire	AL, 5&12mils	AL, 5&12 mils
Mold Material	Epoxy resin with silica filler	Epoxy resin with silica filler
Filler % (Spherical/Flake)	90/10	100/0
Flammability Rating	UL-94 V-0	UL-94 V-0
Backside Metallization	Ti / Ni / Ag	Ti / Ni / Ag
Moisture Level	Up to Level 1 *	Up to Level 1*

Note * based on info provided by assembler and mold compound supplier

III. Result of Reliability Stress for AOL1704 (Standard) & AOL1704L (Green)

Test Item	Test Condition	Time Point	Lot Attribution	Total Sample size	Number of Failures
Solder Reflow Precondition	Standard: 1hr PCT+3 cycle reflow@260°c Green: 168hr 85°c /85%RH +3 cycle reflow@260°c	0hr	Standard: 11 lots	1705 pcs	0
HTGB	Temp = 150°c , Vgs=100% of Vgsmax	168 / 500 hrs 1000 hrs	1 lot (Note A*)	82 pcs 77+5 pcs / lot	0
HTRB	Temp = 150°c , Vds=80% of Vdsmax	168 / 500 hrs 1000 hrs	1 lot (Note A*)	82 pcs 77+5 pcs / lot	0
HAST	130 +/- 2°c , 85%RH, 33.3 psi, Vgs = 80% of Vgs max	100 hrs	Standard: 11 lots (Note B**)	605 pcs 50+5 pcs / lot	0
Pressure Pot	121°c , 29.7psi, RH=100%	96 hrs	Standard: 11 lots (Note B**)	605 pcs 50+5 pcs / lot	0
Temperature Cycle	-65°c to 150°c , air to air,	250 / 500 cycles	Standard: 9 lots (Note B**)	495 pcs 50+5 pcs / lot	0

III. Result of Reliability Stress for AOL1704 (Standard) & AOL1704L (Green) Continues

DPA	Internal Vision Cross-section X-ray	NA	5 5 5	5 5 5	0
CSAM		NA	5	5	0
Bond Integrity	Room Temp 150°c bake 150°c bake	0hr 250hr 500hr	40 40 40	40 wires 40 wires 40 wires	0
Solderability	245°c	5 sec	15	15 leads	0
Die shear	150°c	0hr	10	10	0

Note A: The HTGB and HTRB reliability data presents total of available AOL1704 and AOL1704L burn-in data up to the published date.

Note B: The pressure pot, temperature cycle and HAST reliability data for AOL1704 and AOL1704L comes from the AOS generic package qualification data up to the published date.

IV. Reliability Evaluation

FIT rate (per billion): 128
MTTF = 891years

In general, 500 hrs of HTGB, 150 deg C accelerated stress testing is equivalent to 15 years of lifetime at 55deg C operating conditions (by applying the Arrhenius equation with an activation energy of 0.7eV and 60% of upper confidence level on the failure rate calculation). AOS reliability group also routinely monitors the product reliability up to 1000 hr at and performs the necessary failure analysis on the units failed for reliability test(s).

The presentation of FIT rate for the individual product reliability is restricted by the actual burn-in sample size of the selected product (AOL1704). Failure Rate Determination is based on JEDEC Standard JESD 85. FIT means one failure per billion hours.

$$\text{Failure Rate} = \text{Chi}^2 \times 10^9 / [2 (N) (H) (Af)] = 1.83 \times 10^9 / [2 (164) (168) (258)] = 128$$

$$\text{MTTF} = 10^9 / \text{FIT} = 7.8 \times 10^6 \text{hrs} = 891 \text{years}$$

Chi² = Chi Squared Distribution, determined by the number of failures and confidence interval

N = Total Number of units from HTRB and HTGB tests

H = Duration of HTRB/HTGB testing

Af = Acceleration Factor from Test to Use Conditions (Ea = 0.7eV and Tuse = 55°C)

Acceleration Factor [**Af**] = $\text{Exp} [Ea / k (1/Tj u - 1/Tj s)]$

Acceleration Factor ratio list:

	55 deg C	70 deg C	85 deg C	100 deg C	115 deg C	130 deg C	150 deg C
Af	258	87	32	13	5.64	2.59	1

Tj s = Stressed junction temperature in degree (Kelvin), K = C+273.16

Tj u = The use junction temperature in degree (Kelvin), K = C+273.16

k = Boltzmann's constant, 8.617164 X 10⁻⁵eV / K



V. Quality Assurance Information

Acceptable Quality Level for outgoing inspection: **0.1%** for electrical and visual.

Guaranteed Outgoing Defect Rate: **< 25 ppm**

Quality Sample Plan: conform to **Mil-Std-105D**