



AOS Semiconductor Product Reliability Report

AO4612/AO4612L, rev B

Plastic Encapsulated Device

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This AOS product reliability report summarizes the qualification result for AO4612 . Accelerated environmental tests are performed on a specific sample size, and then followed by electrical test at end point. Review of final electrical test result confirms that AO4612 passes AOS quality and reliability requirements. The released product will be categorized by the process family and be monitored on a quarterly basis for continuously improving the product quality.

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I. Product Description:

The AO4612 uses advanced trench technology MOSFETs to provide excellent $R_{DS(ON)}$ and low gate charge. The complementary MOSFETs may be used in H-bridge, Inverters and other applications. Standard product AO4612 is Pb-free (meets ROHS & Sony 259 specifications). AO4612L is a Green Product ordering option. AO4612 and AO4612L are electrically identical.

Absolute Maximum Ratings $T_A=25^{\circ}\text{C}$ unless otherwise noted					
Parameter	Symbol	Max n-channel	Max p-channel	Units	
Drain-Source Voltage	V_{DS}	60	-60	V	
Gate-Source Voltage	V_{GS}	± 20	± 20	V	
Continuous Drain Current	$T_A=25^{\circ}\text{C}$	I_D	4.5	-3.2	A
	$T_A=70^{\circ}\text{C}$		3.6	-2.6	
Pulsed Drain Current	I_{DM}	20	-20		
Power Dissipation	$T_A=25^{\circ}\text{C}$	P_D	2	2	W
	$T_A=70^{\circ}\text{C}$		1.28	1.28	
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	-55 to 150	$^{\circ}\text{C}$	

Thermal Characteristics					
Parameter	Symbol	Typ	Max	Units	
Maximum Junction-to-Ambient	$R_{\theta JA}$	48	62.5	$T \leq 10\text{s}$	$^{\circ}\text{C/W}$
Maximum Junction-to-Ambient				Steady-State	
Maximum Junction-to-Lead	$R_{\theta JL}$	35	60	Steady-State	$^{\circ}\text{C/W}$
Maximum Junction-to-Ambient	$R_{\theta JA}$	48	62.5	$T \leq 10\text{s}$	$^{\circ}\text{C/W}$
Maximum Junction-to-Ambient				Steady-State	
Maximum Junction-to-Lead	$R_{\theta JL}$	35	40	Steady-State	$^{\circ}\text{C/W}$

II. Die / Package Information:

	AO4612	AO4612 L (Green Compound)
Process	Standard sub-micron Low voltage complementary process	Standard sub-micron
Package Type	8 lead SOIC	8 lead SOIC
Lead Frame	Copper with Solder Plate	Copper with Solder Plate
Die Attach	Silver epoxy	Silver epoxy
Bond wire	2 mils Au wire	2 mils Au wire
Mold Material	Epoxy resin with silica filler	Epoxy resin with silica filler
Filler % (Spherical/Flake)	90/10	100/0
Flammability Rating	UL-94 V-0	UL-94 V-0
Backside Metallization	Ti / Ni / Ag	Ti / Ni / Ag
Moisture Level	Up to Level 1 *	Up to Level 1 *

Note * based on info provided by assembler and mold compound supplier

III. Result of Reliability Stress for AO4612 (Standard) & AO4612 L (Green)

Test Item	Test Condition	Time Point	Lot Attribution	Total Sample size	Number of Failures
Solder Reflow Precondition	Standard: 1hr PCT+3 cycle reflow @260°C Green: 168hr 85°C /85%RH +3 cycles reflow @260°C	0hr	Standard: 49 lots Green: 16 lots	9625 pcs	0
HTGB	Temp = 150°C, Vgs=100% of Vgsmax	168 / 500 hrs 1000 hrs	4 lots (Note A*)	328 pcs 77+5 pcs / lot	0
HTRB	Temp = 150°C, Vds=80% of Vdsmax	168 / 500 hrs 1000 hrs	4 lots (Note A*)	328 pcs 77+5 pcs / lot	0
HAST	130 +/- 2°C, 85%RH, 33.3 psi, Vgs = 80% of Vgs max	100 hrs	Standard: 33 lots Green: 13 lots (Note B**)	2530 pcs 50+5 pcs / lot	0
Pressure Pot	121°C, 15+/-1 PSIG, RH=100%	96 hrs	Standard: 49 lots Green: 16 lots (Note B**)	3575 pcs 50+5 pcs / lot	0
Temperature Cycle	-65 to 150°C, air to air, 0.5hr per cycle	250 / 500 cycles	Standard: 49 lots Green: 15 lots (Note B**)	3520 pcs 50+5 pcs / lot	0

DPA	Internal Vision Cross-section X-ray	NA	5 5 5	5 5 5	0
CSAM		NA	5	5	0
Bond Integrity	Room Temp 150°C bake 150°C bake	0hr 250hr 500hr	40 40 40	40 wires 40 wires 40 wires	0
Solderability	230°C	5 sec	15	15 leads	0

Note A: The HTGB and HTRB reliability data presents total of available AO4612 and AO4612 L burn-in data up to the published date.

Note B: The pressure pot, temperature cycle and HAST reliability data for AO4612 and AO4612 L comes from the AOS generic package qualification data.

IV. Reliability Evaluation

FIT rate (per billion): 13

MTTF = 8781 years

In general, 500 hrs of HTGB, 150 deg C accelerated stress testing is equivalent to 15 years of lifetime at 55 deg C operating conditions (by applying the Arrhenius equation with an activation energy of 0.7eV and 60% of upper confidence level on the failure rate calculation). AOS reliability group also routinely monitors the product reliability up to 1000 hr at and performs the necessary failure analysis on the units failed for reliability test(s).

The presentation of FIT rate for the individual product reliability is restricted by the actual burn-in sample size of the selected product (AO4612). Failure Rate Determination is based on JEDEC Standard JESD 85. FIT means one failure per billion hours.

$$\text{Failure Rate} = \text{Chi}^2 \times 10^9 / [2 (N) (H) (Af)]$$

$$= 1.83 \times 10^9 / [2 (164) (168) (258) + 2 (3 \times 164) (500) (258)] = 13$$

$$\text{MTTF} = 10^9 / \text{FIT} = 7.69 \times 10^7 \text{ hrs} = 8781 \text{ years}$$

Chi² = Chi Squared Distribution, determined by the number of failures and confidence interval

N = Total Number of units from HTRB and HTGB tests

H = Duration of HTRB/HTGB testing

Af = Acceleration Factor from Test to Use Conditions (Ea = 0.7eV and Tuse = 55°C)

Acceleration Factor [**Af**] = **Exp**^[Ea / k (1/Tj u – 1/Tj s)]

Acceleration Factor ratio list:

	55 deg C	70 deg C	85 deg C	100 deg C	115 deg C	130 deg C	150 deg C
Af	258	87	32	13	5.64	2.59	1

Tj s = Stressed junction temperature in degree (Kelvin), K = C+273.16

Tj u = The use junction temperature in degree (Kelvin), K = C+273.16

k = Boltzmann's constant, 8.617164 X 10⁻⁵eV / K



V. Quality Assurance Information

Acceptable Quality Level for outgoing inspection: **0.1%** for electrical and visual.

Guaranteed Outgoing Defect Rate: **< 25 ppm**

Quality Sample Plan: conform to **Mil-Std-105D**